Transient Voltage Suppressors By: DIN Rail Mounted Series Wired AC Unit with Sine Wave Tracking and Discrete All-Mode Protection



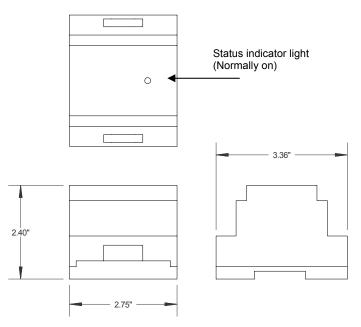
The SineTamer[®] ST-CDIN devices provide the best ring wave transient protection available for a device of its type. These devices are intended for a single 120 or 240 VAC circuit applications at locations feeding sensitive/critical equipment. It is extremely effective in limiting transients generated inside the facility and is an absolute must on circuits feeding critical microprocessor based equipment. It boasts a robust 60kA per phase peak surge current rating.

This economical 35MM DIN rail mounted device is unique in that it is designed as a stand-alone surge suppression device and requires no special external fusing. It is equipped with our board level thermal fusing in combination with our patent-pending component level current fusing as well. Its compact size makes installation a breeze and the warranty is the best in the industry. Add to all that, dedicated "all mode" Enhanced Sinewave Tracking[™] and encapsulated Optimal Response Network[™], and you get a device that defines effective and reliable surge suppression.

We believe that we offer the most versatile TVSS devices on the market with performance specs that are superior to our competitors and a warranty that is second to none, the ST-CDIN units are simply another example of meeting the market demand.

Description:					
Description.	Series wired parallel-connected transient voltage surge suppressor with encapsulated Optimal Response Network™ and Enhanced Sinewave Tracking circuitry (60kA per phase peak surge current.)				
Application:	Designed for use at ANSI/IEEE Category A with susceptibility up to medium exposure levels to protect sensitive/critical loads fed by a single 120 or 240VAC circuit.				
Warranty:	25 Years Unlimited Free Replacement				
Unit Listings:	Tested to UL 1449 Second Edition and CUL				
MECHANICAL	7				
Enclosure:	Housing = Lexan 940 (UL 94V-0); Base = NORYL V01550 (UL 94V-0).				
Mounting:	35MM DIN rails				
Connection Method: Shipping Weight:	Terminal strip at both the input and output sides of the device. 12 AWG – 30 AWB ≈ 2 lbs				
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ELECTRICAL					
	Series wired, parallel connected hybrid design incorporating discrete all mode protection and utilizing our encapsulated O ptimal R esponse N etwork [™] and E nhanced S inewave T racking circuitry design to provide lowest possible let-through-voltages. All suppression circuits are completely encapsulated in our exclusive compound to assure long component life and complete protection from the environment and/or vibration.				
	Dedicated protection components and circuitry for each mode. Discrete L-N (Normal Mode), and Discrete L-G, N-G (Common Mode)				
	50-60Hz				
Frequency:					
Maximum Continuous					
	20 Amps AC				
	<1 nanosecond				
-	Super Bright LED, normally on.				
Circuit Interrupt:	None Required – Board level current fusing standard.				





LET-THROUGH PERFORMANCE AND ELECTRICAL SPECIFICATIONS							
Model	мсоу	Test Mode	ANSI/IEEE C62-41 & C62.45 Let-Through Voltage Test Results				
			A1 2 kV, 67 A 100 KHz Ring Wave 180° Phase Angle	A3 6 kV, 200 A 100 KHz Ring Wave 90° Phase Angle	B3/C1 6 kV, 3 kA Impulse Wave 90° Phase Angle		
ST-CDIN120-20	150 150 150	L-N L-G N-G	30 V (D) 58 V (D) 33 V (S)	N/A	289 (D) 380 (D) 550 (S)		
ST-FDIN120-20	150 150 150	L-N L-G N-G	N/A	210 (D) 290 (D) 430 (S)	289 (D) 380 (D) 550 (S)		
ST-CDIN240-20	300 300 300	L-N L-G N-G	66 V (D) 100 V (D) 33 V (S)	N/A	548 (S) 558 (S) 655 (S)		
ST-FDIN240-20	300 300 300	L-N L-G N-G	N/A	398 (S) 425 (S) 430 (S)	548 (S) 558 (S) 655 (S)		

Let-Through Voltage Test Environment: Dynamic (D) or Static (S) positive Polarity. All voltages are peak (\pm 10%). Time Base is 1 ms. 180° phase angle voltages are measured from the zero crossing, 90° phase angle voltages are measured from the positive peak of the sinewave to the positive peak of the surge indicating actual excess voltage let through. All tests were performed with the device connected in series simulating actual installation.